

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

William P. Moore, et al.

Serial No.: 09/805,138

Filing Date: March 14, 2001

Group Art Unit: 2113

Examiner: Yolanda L. Wilson

For: A METHOD FOR REDUCED POWER CONSUMPTION

Honorable Commissioner of Patents
Alexandria, Virginia 22313-1450

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INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicants' duty of disclosure under 37 CFR §1.56, applicants respectfully bring the following documents listed on the attached from PTO-1449, to the attention of the Examiner in charge of the above-identified application. These references were cited by the Examiner in the divisional Application No. 09/805,200, in an Office Action dated April 16, 2004. Copies of the listed documents are provided herewith for the convenience of the Examiner.

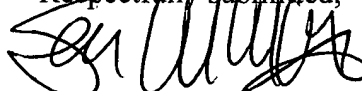
This citation does not constitute an admission that the references are relevant or material to the claim. They are only cited as constituting related art of which the applicants are aware.

I hereby certify that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the information disclosure statement.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge the \$180.00 IDS fee and any deficiencies in fees and credit any overpayment of fees to Assignee's Deposit Account No. 09-0456.

Respectfully submitted,



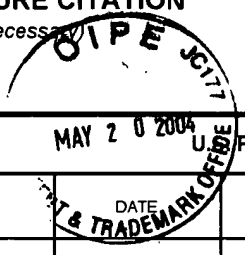
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INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				ATTY DOCKET NO. BUR998050 DIV3		SERIAL NO. 09/805,138	
				APPLICANT(S) William P. Moore, et al.			
				FILING DATE March 14, 2001		GROUP 2113	



U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
	5,046,040	09/91	Akio Miyoshi				
	5,101,344	03/92	Bonet, et al.				
	4,887,203	12/89	MacGregor, et al.				
	4,472,772	09/84	Laurence P. Flora				
	4,484,268	11/84	Thoma, et al.				
	4,713,750	12/87	Damouny, et al.				

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FOREIGN PATENT DOCUMENTS								
DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION			
					YES	NO		

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>		
		Computer Organization and Design- The Hardware/Software Interface; John L. Hennessy and David A. Patterson; Morgan Kaufman Publishers; Second Edition; 1998; Pages B-11 to B-13.
		Computer Architecture-A Quantitative Approach; John L. Hennessy and David A. Patterson; Morgan Kaufman Publishers; Fourth Printing; 1990; Pages 205-206.

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.